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The invention refers to the semiconductor technology, in particular to processes for obtaining semiconductor nanostructures.

The process for obtaining semiconductor nanostructures consists in the electrochemical pickling of semiconductor surfaces. Novelty of the invention consists in that the electrochemical pickling is carried out with the pulse voltage application into a solution with the following component ratio:

H <sub>3</sub> PO <sub>4</sub>	10 ml
HNO <sub>3</sub>	10 ml
Na <sub>2</sub> Cr <sub>2</sub> O <sub>7</sub>	0...10 g
H <sub>2</sub> O	300 ml.

Claims: 1

Fig.: 2