

The invention relates to the computer engineering, namely to the image restoration processors.

The image restoration processor comprises a main object faulted image correction unit (1), a slave object faulted image correction unit (2), eight electronic switches (3), (4), (5), (6), (7), (8), (9) and (10), three NOT gates (11), (12) and (13), three 2-OR gates (14), (15), (16), one 2-AND gate (17), a delay line (18), a main object image input (19), a slave object image input (20), a main object fault indication input (21) and a slave object fault indication input (22), as well as outputs of the main true object (23) and of the slave true object (24).

Claims: 1

Fig.: 3

