

The invention refers to the computer engineering and microelectronics and may be used in the manufacture and operation of microcircuits with built-in means of testing and diagnosis.

The device contains a clock time register 1, a group of counters 2, a biporte on-line storage 3, an adder modulo q 4, a register 5, a group of commutators 6, three OR gates 7, 8 and 9, an AND gate 10, a multiplexer 11, reset input 12, synchronization input 13, data inputs 14 and 15.

Claims: 1

Fig.: 2

