

The invention relates to the field of computer engineering and microelectronics, and can be applied in the production and exploitation of superintegrated circuits with built-in test and diagnosis means.

The device comprises a synchronizing register (1), a counter (2), an on-line storage (3), a group of triggers (4.1, 4.2), a XOR gate (5), a reset input (6), a synchronizing input (7), a delay flip-flop (8), five OR gates (9, 10, 11, 12 and 13), a multiplexer (14), a signature analyzer (15).

Claims: 1

Fig.: 5

